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[Randy Wright, Arun Kumar](#)

October 2000 **WIESS'00: Proceedings of the 1st conference on Industrial Experiences with Systems Software - Volume 1**, Volume 1

**Publisher:** USENIX Association

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

The HP V-Class server family provides up to 32 processors and 32 GB of memory in a single cabinet. Scalable Computing Architecture technology allows multiple V-Class cabinets to be interconnected, forming a single cache coherent non-uniform memory architecture ...

### 22 [Computing curricula 2001](#)

[CORPORATE The Joint Task Force on Computing Curricula](#)

September 2001 **Journal on Educational Resources in Computing (JERI C)**, Volume 1 Issue 3es

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### 23 [Constructing application-specific heterogeneous embedded architectures from custom HW/SW applications](#)

[Steven Vercauteren, Bill Lin, Hugo De Man](#)

June 1996 **DAC '96: Proceedings of the 33rd annual Design Automation Conference**

**Publisher:** ACM [Request Permissions](#)

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24 [Proceedings of the 2000 ACM/IEEE conference on Supercomputing \(CDROM\)](#)

[Jed Donnelley](#)

November **Supercomputing '00**: Proceedings of the 2000 ACM/IEEE conference  
2000 on Supercomputing (CDROM)

**Publisher:** IEEE Computer Society

Additional Information: [full citation](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

25 [Towards a taxonomy of software connectors](#)



[Nikunj R. Mehta](#), [Nenad Medvidovic](#), [Sandeep Phadke](#)

June **ICSE '00**: Proceedings of the 22nd international conference on Software  
2000 engineering

**Publisher:** ACM

Full text available: Pdf (184.27  
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**Bibliometrics:** Downloads (6 Weeks): 19, Downloads (12 Months): 178, Citation Count: 54

Software systems of today are frequently composed from prefabricated, heterogeneous components that provide complex functionality and engage in complex interactions. Existing research on component-based development has mostly focused on component structure, ...

**Keywords:** classification, software architecture, software connector, taxonomy

26 [A strategy for real-time kernel support in application-specific HW/SW embedded](#)



[architectures](#)

[Steven Vercouteren](#), [Bill Lin](#), [Hugo De Man](#)

June **DAC '96**: Proceedings of the 33rd annual Design Automation Conference  
1996

**Publisher:** ACM

Full text available: Pdf (91.67  
KB)

Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 3, Downloads (12 Months): 23, Citation Count: 7

27 [Flexible collaboration transparency: supporting worker independence in replicated application-sharing systems](#)



[James Begole](#), [Mary Beth Rosson](#), [Clifford A. Shaffer](#)

June **Transactions on Computer-Human Interaction (TOCHI)**, Volume 6  
1999 Issue 2

**Publisher:** ACM

Full text available: Pdf (312.22  
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This article presents a critique of conventional collaboration transparency systems, also called "application-sharing" systems, which provide the real-time shared use of legacy single-user applications. We find that conventional collaboration ...

**Keywords:** Flexible JAMM, Java, application sharing, collaboration transparency, computer-supported cooperative work, groupware, usability


28 [Using Postfix for Secure SMTP Gateways](#)

[Mick Bauer, Brenno de Winter](#)

October 2000

**Linux Journal** , Volume 2000 Issue 78s

**Publisher:** Specialized Systems Consultants, Inc.

Full text available:  [HTML](#) (25.85 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 4, Downloads (12 Months): 19, Citation Count: 0

Improve your site's e-mail hygiene and make life difficult for spammers and hackers.

29 [Mixin layers: an object-oriented implementation technique for refinements and collaboration-based designs](#)



[Yannis Smaragdakis, Don Batory](#)


April

**Transactions on Software Engineering and Methodology (TOSEM)** ,

2002

Volume 11 Issue 2

**Publisher:** ACM 

Full text available:  [PDF](#) (510.43 KB)

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**Bibliometrics:** Downloads (6 Weeks): 33, Downloads (12 Months): 141, Citation Count: 36

A "refinement" is a functionality addition to a software project that can affect multiple dispersed implementation entities (functions, classes, etc.). In this paper, we examine large-scale refinements in terms of a fundamental object-oriented technique ...

**Keywords:** Collaboration-based design, component-based software, product-line architectures

30 [Design methodology for PicoRadio networks](#)

[J. da Silva, Jr., J. Shamberger, M. Ammer, C. Guo, S. Li, B. Shah, T. Tuan, M. Sheets, J. Rabaey, B. Nikolic, A. Sangiovanni-Vincentelli, P. Wright](#)


March

**DATE '01** : Proceedings of the conference on Design, automation and test in

2001

Europe

**Publisher:** IEEE Press

Full text available:  [PDF](#) (328.60 KB)

Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 6, Downloads (12 Months): 23, Citation Count: 2

31 [Future of database systems: changing applications and technological developments](#)




[M. Tamer Ozsu](#)

December

**Computing Surveys (CSUR)** , Volume 28 Issue 4es

1996

**Publisher:** ACM 

Full text available:  [HTML](#) (18.91 KB)

Additional Information: [full citation](#), [references](#)

**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 42, Citation Count: 0

### 32 [A hierarchical modeling framework for on-chip communication architectures](#)



[Xinping Zhu](#), [Sharad Malik](#)

November 2002 **ICCAD '02**: Proceedings of the 2002 IEEE/ACM international conference on Computer-aided design

**Publisher:** ACM

Full text available: [Pdf](#) (124.52 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 42, Citation Count: 19

The communication sub-system of complex IC systems is increasingly critical for achieving system performance. Given this, it is important that the on-chip communication architecture should be included in any quantitative evaluation of system design during ...

### 33 [Network support for mobile multimedia using a self-adaptive distributed proxy](#)



[Zhaoqing Morley Mao](#), [Hoi-sheung Wilson So](#), [Byunghoon Kang](#)

January 2001 **NOSSDAV '01**: Proceedings of the 11th international workshop on Network and operating systems support for digital audio and video

**Publisher:** ACM

Full text available: [Pdf](#) (212.65 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 3, Downloads (12 Months): 30, Citation Count: 5

Recent advancements in video and audio codec technologies~ (e.g., RealV ideo [18] make multimedia streaming possible across a wide range of network conditions. With an increasing trend of ubiquitous connectivity, more and more areas have overlapping coverage ...

### 34 [Component-based design approach for multicore SoCs](#)



[W. Cesário](#), [A. Baghdadi](#), [L. Gauthier](#), [D. Lyonnard](#), [G. Nicolescu](#), [Y. Paviot](#), [S. Yoo](#), [A. A. Jerraya](#), [M. Diaz-Nava](#)

June 2002 **DAC '02**: Proceedings of the 39th annual Design Automation Conference

**Publisher:** ACM

Full text available: [Pdf](#) (187.82 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 13, Downloads (12 Months): 149, Citation Count: 31

This paper presents a high-level component-based methodology and design environment for application-specific multicore SoC architectures. Component-based design provides primitives to build complex architectures from basic components. This bottom-up ...

**Keywords:** HW/SW interfaces abstraction, component-based design, multicore System-on-Chip

35 [Prefetching for improved bus wrapper performance in cores](#)



[Roman Lysecky, Frank Vahid](#)

January 2002 **Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 7 Issue 1

**Publisher:** ACM [Request Permissions](#)

Full text available: [Pdf](#) (430.83 KB)

**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 10, Downloads (12 Months): 28, Citation Count: 4

Reuse of cores can reduce design time for systems-on-a-chip. Such reuse is dependent on being able to easily interface a core to any bus. To enable such interfacing, many propose separating a core's interface from its internals by using a bus wrapper. ...

**Keywords:** Bus wrapper, PPCI, VSI, cores, design reuse, intellectual property, interfacing, on-chip bus, system-on-a-chip

36 [Mining specifications](#)



[Glenn Ammons, Rastislav Bodik, James R. Larus](#)

January 2002 **POPL '02: Proceedings of the 29th ACM SIGPLAN-SIGACT symposium on Principles of programming languages**

**Publisher:** ACM [Request Permissions](#)

Full text available: [Pdf](#) (305.87 KB)

**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited by](#)

**Bibliometrics:** Downloads (6 Weeks): 19, Downloads (12 Months): 161, Citation Count: 81

Program verification is a promising approach to improving program quality, because it can search all possible program executions for specific errors. However, the need to formally describe correct behavior or errors is a major barrier to the widespread ...

Also published in:

January 2002 **SIGPLAN Notices** Volume 37 Issue 1

37 [The click modular router](#)



[Eddie Kohler, Robert Morris, Benjie Chen, John Jannotti, M. Frans Kaashoek](#)

August 2000 **Transactions on Computer Systems (TOCS)**, Volume 18 Issue 3

**Publisher:** ACM [Request Permissions](#)

Full text available: [Pdf](#) (376.31 KB)

**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 32, Downloads (12 Months): 280, Citation Count: 222

Clicks is a new software architecture for building flexible and configurable routers. A Click router is assembled from packet processing modules called elements. Individual elements implement simple router functions like packet classification, ...

**Keywords:** component systems, routers, software router performance

38 [Adaptable query optimization and evaluation in temporal middleware](#)



[Giedrius Silivinskas](#), [Christian S. Jensen](#), [Richard Thomas Snodgrass](#)

June **SIGMOD '01**: Proceedings of the 2001 ACM SIGMOD international  
2001 conference on Management of data

**Publisher:** ACM [Request Permissions](#)

Full text available: [PDF](#) (232.65  
KB)

**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited by](#),  
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**Bibliometrics:** Downloads (6 Weeks): 10, Downloads (12 Months): 64, Citation Count: 1

Time-referenced data are pervasive in most real-world databases. Recent advances in temporal query languages show that such database applications may benefit substantially from built-in temporal support in the DBMS. To achieve this, temporal query optimization ...

Also published in:

June 2001 **SIGMOD Record** Volume 30 Issue 2

39 [A codesign back-end approach for embedded system design](#)



[G. Gogniat](#), [M. Auguin](#), [L. Bianco](#), [A. Pegatoquet](#)

July **Transactions on Design Automation of Electronic Systems (TODAES)**,  
2000 Volume 5 Issue 3

**Publisher:** ACM [Request Permissions](#)

Full text available: [PDF](#) (148.70  
KB)

**Additional Information:** [full citation](#), [abstract](#), [references](#), [index](#),  
[terms](#)

**Bibliometrics:** Downloads (6 Weeks): 9, Downloads (12 Months): 34, Citation Count: 2

Continuous advances in processor and ASIC technologies enable the integration of more and more complex embedded systems. Since their implementations generally require the use of heterogeneous resources (e.g., processor cores, ASICs) in one system with ...

**Keywords:** HW/SW integration, codesign, communications synthesis, template architecture

40 [The Modelling of Embedded Systems Using HASoC](#)

[M. Edwards](#), [P. Green](#)

March **DATE '02**: Proceedings of the conference on Design, automation and test in  
2002 Europe

**Publisher:** IEEE Computer Society

Full text available: [PDF](#) (195.94  
KB)



**Additional Information:** [full citation](#), [abstract](#), [cited by](#)

**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 28, Citation Count: 3

We present a design method (HASoC) for the lifecycle modelling of embedded systems that are targeted primarily, but not necessarily, at SoC implementations. The object-oriented development technique is based on our experiences of using an existing modelling ...

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